



IFW

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Brian G. Johnson

Serial No.: 10/633,874

Filed: August 4, 2003

For: Lateral Phase Change
Memory

§
§
§
§
§
§
§
§
§

Art Unit: 2826

Examiner: Thomas L. Dickey

Docket: ITO.0047US
P16202

Assignee: Intel Corporation

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the restriction requirement mailed December 28, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: January 9, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden
Cynthia L. Hayden